



586  
126  
12-30-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/943,180  
Filing Date .... August 29, 2001  
Inventor .... Moore et al.  
Assignee .... Micron Technology, Inc.  
Group Art Unit .... 2825  
Examiner .... Unknown  
Attorney's Docket No. .... MI22-1776  
Title: Capacitors, Methods of Forming Capacitors, and Methods of Forming Capacitor Dielectric Layers

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted

Dated: 12-6-02

By: 

Mark S. Matkin, P.S.  
Reg. No. 32,268

RECEIVED  
DEC 10 2002  
TC 2800 MAIL ROOM

EV182657068